

## PATENT COOPERATION TREATY

PCT



## INTERNATIONAL SEARCH REPORT

(PCT Article 18 and Rules 43 and 44)

Applicant's or agent's file reference <b>H1048</b>	<b>FOR FURTHER ACTION</b>	
see Form PCT/ISA/220 as well as, where applicable, item 5 below.		
International application No. <b>PCT/US2005/006174</b>	International filing date (day/month/year) <b>26/02/2005</b>	(Earliest) Priority Date (day/month/year) <b>02/03/2004</b>
Applicant <b>ADVANCED MICRO DEVICES, INC.</b>		

This International Search Report has been prepared by this International Searching Authority and is transmitted to the applicant according to Article 18. A copy is being transmitted to the International Bureau.

This International Search Report consists of a total of 3 sheets.

It is also accompanied by a copy of each prior art document cited in this report.

**1. Basis of the report**

a. With regard to the language, the international search was carried out on the basis of the international application in the language in which it was filed, unless otherwise indicated under this item.

The international search was carried out on the basis of a translation of the international application furnished to this Authority (Rule 23.1(b)).

b.  With regard to any nucleotide and/or amino acid sequence disclosed in the international application, see Box No. I.

2.  Certain claims were found unsearchable (See Box II).

3.  Unity of invention is lacking (see Box III).

4. With regard to the title,

the text is approved as submitted by the applicant.

the text has been established by this Authority to read as follows:

5. With regard to the abstract,

the text is approved as submitted by the applicant.

the text has been established, according to Rule 38.2(b), by this Authority as it appears in Box No. IV. The applicant may, within one month from the date of mailing of this international search report, submit comments to this Authority.

6. With regard to the drawings,

a. the figure of the drawings to be published with the abstract is Figure No. 1.

as suggested by the applicant.

as selected by this Authority, because the applicant failed to suggest a figure.

as selected by this Authority, because this figure better characterizes the invention.

b.  none of the figures is to be published with the abstract.

## INTERNATIONAL SEARCH REPORT

International Application No  
PCT/US2005/006174

**A. CLASSIFICATION OF SUBJECT MATTER**  
INV. G06F17/14

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)  
G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>SON B S ET AL: "A high-speed FFT processor for OFDM systems"            PROCEEDINGS OF THE 2002 INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS (ISCAS 2002), 26-29 MAY 2002, PHOENIX-SCOTTSDALE, AZ, USA,            vol. 3, 26 May 2002 (2002-05-26), pages 281-284, XP002396821            IEEE, USA            abstract            section 3</p> <p>-----</p> <p style="text-align: center;">-/-</p>	1-10

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

\* Special categories of cited documents :

- \*'A\* document defining the general state of the art which is not considered to be of particular relevance
- \*'E\* earlier document but published on or after the international filing date
- \*'L\* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- \*'O\* document referring to an oral disclosure, use, exhibition or other means
- \*'P\* document published prior to the international filing date but later than the priority date claimed

- \*'T\* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- \*'X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- \*'Y\* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- \*'8\* document member of the same patent family

Date of the actual completion of the international search

30 August 2006

Date of mailing of the international search report

12/09/2006

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
 NL - 2280 HV Rijswijk  
 Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
 Fax: (+31-70) 340-3016

Authorized officer

Domingo Vecchioni, M

## INTERNATIONAL SEARCH REPORT

International Application No PCT/US2005/006174
---

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>HSIN-FU LO ET AL: "Design of an efficient FFT processor for DAB system"          PROCEEDINGS OF THE 2001 INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS (ISCAS 2001), 6-9 MAY 2001, SYDNEY, NSW, AUSTRALIA,          vol. 4, 6 May 2001 (2001-05-06), pages 654-657, XP002396822          IEEE, USA          the whole document</p> <p>-----</p>	1-10
X	<p>JOHNSON L G: "Conflict free memory addressing for dedicated FFT hardware"          IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: ANALOG AND DIGITAL SIGNAL PROCESSING,          vol. 39, no. 5, 1 May 1992 (1992-05-01),          pages 312-316, XP000305260          IEEE, USA          ISSN: 1057-7130          section I          figures 1,2          section III, equation (7)</p> <p>-----</p>	1-10
A	<p>YUTAI MA ET AL: "A Hardware Efficient Control of Memory Addressing for High-Performance FFT Processors"          IEEE TRANSACTIONS ON SIGNAL PROCESSING,          vol. 48, no. 3, March 2000 (2000-03),          pages 917-921, XP011058892          IEEE, USA          ISSN: 1053-587X          section II          sections III.A and III.B</p> <p>-----</p>	4,8,9